

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. – 3. (Canceled)

4. (Currently Amended) ~~A semiconductor integrated circuit according claim 1~~ A semiconductor integrated circuit for processing a plurality of received broadcast signals, the broadcast signals being of a type each having a different respective digital code, the semiconductor integrated circuit comprising:

a digital sampler;

a memory arrangement; and

a plurality of correlators, being arranged to be operable in two modes wherein:

in an acquisition mode:

the digital sampler is adapted to sample the received broadcast signals to produce a digital bit stream at a first bit rate;

the memory arrangement is adapted to receive the digital bit stream and to output at a second bit rate, being higher than the first bit rate;

the plurality of correlators is adapted to receive the digital bit stream at the second bit rate, and each of the plurality of correlators is adapted to correlate the digital bit stream with a same locally generated version of one of the different digital codes; and

in a track mode:

the digital sampler is adapted to sample the received broadcast signals to produce a digital bit stream at the first bit rate and to provide the digital bit stream direct to each of the plurality of correlators, each correlator is adapted to correlate the digital bit stream with a different locally generated version of one of the digital codes,

wherein the memory arrangement ~~comprises~~ includes two shift registers arranged to alternately receive the digital bit stream at the first bit rate while another of the shift registers circulates at the second bit rate.

5. (Canceled)

6. (Currently Amended) ~~A~~ The method according to claim ~~5-7~~ wherein providing the digital bit stream at the second bit rate ~~comprises~~ includes circulating successive portions of the bit stream in ~~a~~ said circulating shift register at the second bit rate.

7. (Currently Amended) ~~A method according to claim 5~~ A method of processing a plurality of received broadcast signals each showing a different respective digital code, the method comprising:

sampling the received broadcast signals to produce a digital bit stream at a first bit rate;

providing the digital bit stream at a second bit rate by reading into a memory arrangement at the first bit rate and reading out at the second bit rate;

correlating the digital bit stream at the second bit rate using a plurality of correlators each correlating the digital bit stream with a same one of a locally generated version of the digital codes to acquire the broadcast signals; and

subsequently correlating the digital bit stream at the first bit rate using the plurality of correlators each correlating the digital bit stream with a locally generated version of a different one of the digital codes to track previously acquired broadcast signals,

wherein providing the digital bit stream at the second bit rate ~~comprises~~ includes alternately reading the bit stream at the first bit rate into one of two shift registers while another of the two shift registers circulates at the second bit rate.

8. (Canceled)

9. (Currently Amended) The apparatus of claim ~~8-15~~ wherein the correlator unit ~~comprises~~ includes a plurality of correlators, each to correlate the received digital bit stream with a same one of the digital codes.

10. (Currently Amended) The apparatus of claim ~~8-15~~ wherein the one of the digital codes used in ~~the~~ correlation in the acquisition mode ~~comprises~~ includes a locally generated version of the digital code.

11. (Currently Amended) The apparatus of claim ~~8-15~~ wherein the one of the digital codes used in ~~the~~ correlation in the track mode ~~comprises~~ includes a locally generated version of the digital code.

12. (Currently Amended) The apparatus of claim ~~8-15~~ wherein the second bit rate is higher than the first bit rate.

13. – 14. (Canceled)

15. (Currently Amended) ~~The apparatus of claim 8~~ An apparatus to process a plurality of received broadcast signals having digital codes, the apparatus comprising:

a sampler to sample the received broadcast signals to produce a digital bit stream at a first bit rate in an acquisition mode;

a memory unit coupled to the sampler to receive the digital bit stream therefrom and to output the digital bit stream at a second bit rate in the acquisition mode; and

a correlator unit coupled to the memory unit to receive the digital bit stream at the second bit rate and to correlate the received digital bit stream with one of the digital codes in the acquisition mode, and wherein the sampler can directly provide the digital bit stream at the first bit rate to the correlator unit in a track mode to allow the correlator to correlate that bit stream to a different one of the digital codes,

wherein the memory unit comprises a plurality of shift registers to alternately receive the digital bit stream at the first bit rate, while another of these shift registers circulates at the second bit rate.

16. (Canceled)

17. (Currently Amended) The system of claim ~~16-18~~ wherein the means for producing the digital stream at the second bit rate ~~comprises a means for circulating~~circulates successive portions of the bit stream at the second bit rate.

18. (Currently Amended) ~~The system of claim 16~~ A system for processing a plurality of received broadcast signals having digital codes, the system comprising:

means for sampling the received broadcast signals to produce a digital bit stream at a first bit rate;

means for receiving the digital bit stream at the first bit rate and for producing the digital bit stream at a second bit rate; and

means for correlating the digital bit stream at the second bit rate with one of the digital codes to acquire the broadcast signals, and for correlating the digital bit stream at the first bit rate with a different one of the digital codes to track the acquired broadcast signals,

wherein the means for producing the digital stream at the second bit rate ~~comprises a~~ includes means for alternately reading the bit stream at the first bit rate into a plurality of shift registers while one of these shift registers circulates at the second bit rate.

19. (Currently Amended) The system of claim ~~16-18~~ wherein the means for correlating the bit streams at the first and second bit rates ~~comprises~~ includes a plurality of correlator means for respectively correlating the bit streams with locally generated versions of the digital codes.

20. (Currently Amended) The system of claim ~~16~~18 wherein the second bit rate is higher than the first bit rate.

21. (New) The system of claim 18 wherein the broadcast signals include global position system (GPS) signals.

22. (New) The circuit of claim 4 wherein the second bit rate is higher than the first bit rate.

23. (New) The circuit of claim 4 wherein said another of the shift registers that circulates at the second bit rate is adapted to circulate successive portions of the bit stream.

24. (New) The circuit of claim 4 wherein the broadcast signals include global position system (GPS) signals.

25. (New) The method of claim 7 wherein the second bit rate is higher than the first bit rate.

26. (New) The method of claim 7 wherein the broadcast signals include global position system (GPS) signals.

27. (New) The apparatus of claim 15 wherein the broadcast signals include global position system (GPS) signals.